



Applied Seals NA, Inc. Announces Micron Global Account Manager

Newark, CA November 22, 2010 – Applied Seals NA, Inc. (ASNA), a leading supplier of high performance elastomeric seals and sealing solutions, is proud to announce that Jeffrey Blouse has taken responsibility for managing Micron Global Accounts. Jeff has over 15 years experience in semiconductor process technology, sales and business development. In his new role, Jeff will develop and implement account strategy for Micron Technologies and ASNA while leading a global team of Applied Seals technologists, distributors and representatives. He brings with him a strong record of Global Account Management from key industry participants such as Novellus Systems, Cymer, and IBM.

“I look forward to partnering with Micron Technologies, a “Best in Class” organization,” was Jeff’s reaction to being awarded the Micron account.

Dalia Vernikovsky, CEO and General Manager of Applied Seals, NA echoes Jeff’s comments saying, “Jeff will strengthen an already strong alliance between the companies. It is especially exciting to have the opportunity to align two technical leaders in their respective fields and Jeff’s background is ideally suited to work with Micron and their partners.”

Bio

Jeff Blouse joined Applied Seals NA, Inc. in 2010. He has more than 15 years of experience as a semiconductor technology professional. This includes hands-on process engineering experience for IBM and Novellus Systems, Inc. and 5 years at Cymer, Inc. While at Cymer, Mr. Blouse's responsibilities included being a Field Project Manager for Cymer's EUV program at Albany Nanotech and introducing 1st of a kind service and technology upgrades to the customer base. While at IBM Corporation, he served as an advisory engineer defining process integration, process development and production as well as all phases of IC design.

Mr. Blouse holds a B.S. in chemistry from Juniata College and has received Invention Achievement Awards in 1989 and 1991 and co-authored four US Patents including IBM's original Silicon/Germanium device design.



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